

In the United States Patent and Trademark Office

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Applicant(s): Mostafazadeh, Shahram; Smith, Joseph O.
Assignee: National Semiconductor Corp.
Title: LEAD FRAME DESIGN FOR INCREASED CHIP PINOUT
Serial No.: 09/054,380 Filed: April 2, 1998
Examiner: H. Duong Group Art Unit: 2822
Docket No.: NS-3856 US

San Jose, California
July 8, 1999

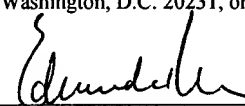
ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

PETITION FOR EXTENSION OF TIME

Dear Sir:

Applicants respectfully petition for a three-month extension of time within which to respond to the January 21, 1999 outstanding Office Action, such extension allowing the undersigned until July 21, 1999 to respond.

Please charge our Deposit Account No. 19-2386 in the amount of \$870.00 as set forth in the enclosed transmittal letter.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: ASSISTANT COMMISSIONER FOR PATENTS, Washington, D.C. 20231, on <u>July 8</u> , 19 <u>99</u> .	
 Attorney for Applicant(s)	<u>7/8/99</u> Date of Signature

Respectfully submitted,



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Attorney for Applicant(s)
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